

```

1 ;-----
2 ; Code Produced by the Proton Compiler. Ver 3.5.6.2
3 ; Copyright Rosetta Technologies/Crownhill Associates
  Ltd
4 ; Written by Les Johnson. December 2013
5 ;-----
5196 #define TMR1_VALH 216
5197 HSERIAL_SPBRG = 25
6657 ram_bank = 15
6658 clrf CM1CON,1
6659 clrf CM2CON,1
6660 movlw 15
6661 movwf ADCON1,0
6662 clrf ANCON0,1
6663 clrf ANCON1,1
6664 F2_000085 equ $ ; IN [GT_AMPERE_DEVICE.INC]
      ADCON1 = %00011000

```

```

1 ;-----
2 ; Code Produced by the Proton Compiler. Ver 3.5.6.7
3 ; Copyright Rosetta Technologies/Crownhill Associates Ltd
4 ; Written by Les Johnson. May 2014
5 ;-----
5196 #define TMR1_VALH 216
5197 #define __SYSCOM_ALCD_PRINT_REQ_1
5198 #define __SYSCOM_ALCD_HELPERS_REQ_1
5199 #define __SYSCOM_ALCD_CURSOR_REQ_1
5200 HSERIAL_SPBRG = 25
6660 ram_bank = 15
6661 setf ANCON0,1
6662 movlw 127
6663 movwf ANCON1,1
6664 F2_000085 equ $ ; IN [GT_AMPERE_DEVICE.INC]
      ADCON1 = %00011000

```