## **TECH BRIEF**

## The Premise

The computer has extended the capabilities of electronic products and systems by adding new dimensions of control, computation and programmability. The world of analog products, however, has not been able to take advantage of these advances. We can have the best of both worlds by combining Xicor digitally controlled potentiometers (XDCPs<sup>™</sup>) with standard analog functions. The XDCP allows the designer to "computerize" the analog function by PUTTING ANALOG ON THE BUS!



If the parameters of the analog function are varied using the XDCP, and the XDCP is controlled by a microprocessor, then the analog function is now digitally controlled.

ANY analog function whose functional specification depends on resistance is a candidate for "computerization" using XDCPs!



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## The Design: IMPLEMENTING PROGRAMMABLE BANDPASS FILTERS USING XDCPs

A common requirement in many signal processing applications is a bandpass filter network to precondition an input signal to a desired range of frequencies. Traditionally, this circuit has been implemented using basic analog building blocks (e.g. operational amplifiers) in conjunction with fixed resistors and capacitors to set the center frequency ( $f_0$ ) of the bandpass filter.

The figure below shows a programmable infinite gain multiple feedback (IGMF) bandpass filter. These filters are characterized by a fixed, five component configuration.



For this circuit, the gain  $V_0/V_s$  is given by

$$\frac{V_0}{V_5} = \frac{-s/R_1C}{s^2 + s(2/R_3C) + (R_1 + R_2)/R_1R_2R_3C^2} = \frac{-A_0s(2\pi f_0/Q)}{s^2 + s(2\pi f_0/Q) + 2\pi f_0^2}$$

where  $A_0$ ,  $f_0$  and Q represent the passband gain, characteristic frequency and figure of merit, respectively. A problem common to most second order, active filters is the dependence of each of the filters parameters on the values of all or most circuit components. Ideally, one component would control a single parameter.

As the equation below shows, the filter center frequency  $f_0$  controlled by  $R_2$  is independent of gain  $A_0$ , and bandwidth.

$$f_{0} = \ \frac{1}{2\pi C} \ \sqrt{\frac{R_{1} + R_{2}}{R_{1}R_{2}R_{3}}} \ , \ 2.5 KHz \leq f_{0} \leq 12.5 KHz$$

where  $R_2$  is an XDCP, and  $R_1$  and  $R_3$  are fixed resistors used to set the gain and passband bandwidth of the filter. Hence, varying XDCP  $R_2$  will change the center frequency, but not the gain or bandwidth of the filter.



A Xicor Design Education Series

## Xicor Mixed Signal Products-Digitally Controlled Potentiometers

PRODUCT	DESCRIPTION	PKG	TEMP	IC	ISB	Vu/Vu	V+	V-	DATA	SAMPLES	PRODUCTION
11100001		CODE*	RANGE	(mA)	(μA)			-	SHEET	CARTA ELO	
Quad 256	Гар										
X9250U,T	Quad 256 tap XDCP, low power, SPI interface, dual supply	S24	C,I	0.4	1	-5.5V to +5.5V	+2.7V to	-2.7V to	NOW	NOW	NOW
X9258U,T	Quad 256 tap XDCP, low power, two-wire interface, dual supply	S24	C,I	0.4	1	-5.5V to +5.5V	+5.5V +2.7V to	-5.5V -2.7V to	NOW	NOW	NOW
Quad 64 Tap											
X9241Y,W	Quad 64 tap XDCP, two-wire interface, single supply	P(20) S(20) V(20)	C,I	3	500	-5.5V to	N/A	N/A	NOW	NOW	NOW
X9400Y,W	Quad 64 tap XDCP, low power, SPI interface, dual supply	S24, V24	C,I	0.4	1	-5.5V to	+2.7V to	-2.7V to	NOW	NOW	NOW
X9401W	Quad 64 tap XDCP, low power, SPI interface, single supply	S24, V24	C,I	0.4	1	0V to +5.5V	N/A	N/A	NOW	NOW	NOW
X9408Y,W	Quad 64 tap XDCP, low power, two-wire interface, dual supply	S24, V24	C,I	0.4	1	-5.5V to	+2.7V to	-2.7V to	NOW	NOW	NOW
X9409W	Quad 64 tap XDCP, low power, two-wire interface,	S24, V24	C, I	0.4	1	+5.5V OV to	+5.5V N/A	-5.5V N/A	NOW	NOW	NOW
single supply +5.5V											
Dual 64 la	p										
X9221Y, W,U	Dual 64 tap XDCP, two-wire interface, single supply	P(20) S(20)	C,I	3	500	-5.5V to +5.5V	N/A	N/A	NOW	NOW	NOW
X9410Y,W	Dual 64 tap XDCP, low power, SPI interface, dual supply	S24, V24	C,I	0.4	1	-5.5V to	+2.7V to	-2.7V to	NOW	NOW	NOW
X9411W	Dual 64 tap XDCP, low power, SPI interface, single supply	S24, V24	C,I	0.4	1	0V to +5.5V	+5.5V N/A	-5.5V N/A	NOW	NOW	NOW
X9418Y,W	Dual 64 tap XDCP, low power, two-wire interface, dual supply	S24, V24	C,I	0.4	1	-5.5V to	+2.7V to	-2.7V to	NOW	NOW	NOW
						+5.5V	+5.5V	+5.5V			
X9419W	Dual 64 tap XDCP, low power, two-wire interface, single supply	S24, V24	C,I	0.4	1	0V to +5.5V	N/A	N/A	NOW	Q4	Q1/00
Single 100	) Тар										
X9C102	Single 100 tap XDCP, 1k $\Omega$ increment/decrement interface, single supply	P, S	C, I	3	500	-5.5V to +5.5V	N/A	N/A	NOW	NOW	NOW
X9C103	Single 100 tap XDCP, 10k $\Omega$ increment/decrement interface, single supply	P, S	C, I	3	500	-5.5V to +5.5V	N/A	N/A	NOW	NOW	NOW
X9C104	Single 100 tap XDCP, 100k $\Omega$ increment/decrement interface, single supply	P, S	C, I	3	500	-5.5V to +5.5V	N/A	N/A	NOW	NOW	NOW
X9C303	Single 100 tap XDCP, 32k $\Omega$ increment/decrement interface, single supply	P, S V8	C, I	3	500	-5.5V to +5.5V	N/A	N/A	NOW	NOW	NOW
X9C503	Single 100 tap XDCP, 50k $\Omega$ increment/decrement interface, single supply	P, S	C, I	3	500	-5.5V to +5.5V	N/A	N/A	NOW	NOW	NOW
X9312W, Z,T	Single 100 tap XDCP, 1k Ω increment/decrement interface, 0-15V terminal voltage single supply	P, S, No V	C, I	3	1000	0 to +15V	N/A	N/A	NOW	NOW	NOW
X9317W,U	Single 100 tap XDCP, low power increment/decrement interface, single supply	P, S8 V8	C, I	0.4	1	0V to +5.5V	N/A	N/A	NOW	NOW	NOW
Single 64	Гар										
X9420Y,W	Single 64 tap XDCP, low power SPI interface, dual supply	S16, V14	C, I	0.4	1	-5.5V to	+2.7V to	-2.7V to	NOW	NOW	NOW
X9421W	Single 64 tap XDCP, low power SPI interface, single supply	S16, V14	C, I	0.4	1	0V to +5.5V	N/A	N/A	NOW	Q4	Q1/00
X9428Y,W	Single 64 tap XDCP, low power two-wire interface, dual supply	S16, V14	C, I	0.4	1	-5.5V to +5.5V	+2.7V to +5.5V	-2.7V to -5.5V	NOW	NOW	NOW
X9429W	Single 64 tap XDCP, low power two-wire interface, single supply	S16, V14	C, I	0.4	1	0V to +5.5V	N/A	N/A	NOW	Q4	Q1/00
Single 32	Тар										
X9313Z,	Single 32 tap XDCP, increment/decrement interface single	M, P, S	C, I C	3	500	-5V to +5V	N/A	N/A	NOW	NOW	NOW
X9315W,N	Suppry Single 32 tap XDCP, low power increment/decrement interface,	M, P, S(8)	C, I	0.4	1	0V to +5.5V	N/A	N/A	NOW	NOW	NOW
Single 16 Tap											
X9116W	Single 16 tap XDCP, low power increment/decrement interface, single supply	S8, M8	C, I	0.4	1	0V to +5.5V	N/A	N/A	NOW	NOW	NOW
32 Tap PushPots											
X9511W,Z	Single 32 tap pushpot XDCP, pushbutton interface, single supply	P, S	C, I	3	500	-5V to +5V	N/A	N/A	NOW	NOW	NOW
Comparato	rs										
X9440Y,W	Dual smart comparator with dual 64 tap XDCP, low power SPI	S24, V24	C, I	0.4	1	-5.5V to	+2.7V to	-2.7V to	NOW	NOW	NOW
X9448Y,W	Dual smart comparator with dual 64 tap XDCP, low power two.wire interface, dual supply	S24, V24	C, I	0.4	1	+5.5V -5.5V to	+3.5V +2.7V to	-5.5V -2.7V to	NOW	NOW	NOW
64 tan ana	rational amplifiors					+0.0V	+0.0V	-9.94			
04 tap ope											
X9430W	Dual operational amplifier with dual 64 tap XDCP, low power SPI interface, dual supply	S24, V24	C, I	0.4	1	-5.5V to +5.5V	+2.7V to +5.5V	-2.7V to -5.5V	NOW	NOW	NOW
X9438W	Dual operational amplifier with dual 64 tap XDCP, low power two-wire interface, dual supply	S24, V24	C, I	0.4	1	-5.5V to +5.5V	+2.7V to +5.5V	-2.7V to -5.5V	NOW	NOW	NOW

\*PKG Codes: M=MSOP, P=PDIP, S=SOIC, V=TSSOP Resistor Values (Ohms): Z=1K, Y=2K, W=10K, U=50K, T=100K, M=2K, 10K, 10K, 50K, N=500K



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